

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------|------------------|---------|------------------|
| L1 | 275 | 380/29.ccls. and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:20 |
| L2 | 269 | 380/37.ccls. and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:51 |
| L3 | 2 | ("6870929").PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/07/22 14:23 |
| L4 | 1006 | ((("6671771").PN.) or "5673319".PN. or "5673319".PN. or "5548648".PN. or "5513262".PN. or "5513262".URPN. or "5513262".PN. or "5481613".PN. or "5481613".URPN. or "5373559".PN. or "5373559".PN. or "5341426".PN. or "5341426".URPN. or "5315658".PN. or "5315658".URPN. or "5200999".PN. or "5200999".URPN. or "4607137".PN. or "4607137".URPN. or "4405829".PN. or "4405829".URPN. or "4200770".PN. or "4200770".URPN. or "5703952".PN. or "5703952".URPN. or "5673319".PN. or "5673319".URPN. or "5671284".PN. or "5671284".URPN. or "5671284".PN. or "5623549".PN. or "5623549".URPN. or "5606616".PN. or "5606616".URPN. or "5214703".PN. or "5214703".URPN. or "5384848".PN. or "5384848".URPN. or "5606616".PN. or "4995082".PN. or "4933969".PN. or "4933969".URPN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:29 |
| L5 | 59 | (L1 or L2) and L4 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:32 |

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| L6 | 4185 | ((hardware or IC or circuit or chip) with (encrypt\$3 or decrypt\$3 or crypto\$)).ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:40 |
| L7 | 1467 | L6 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:35 |
| L8 | 348 | L7 and ("380"/\$.ccls. or "713"/\$.ccls. or "726"/\$.ccls.) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:52 |
| L9 | 28 | L8 and (L1 or L2) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:46 |
| L10 | 355 | "6870929" "6549622" "6327654" "6081895" "6069956" "6038317" "6028939" "6028933" "5848154" "5687237" "5647001" RE35403 "5381480" "5317639" "5214704" "5121429" "5063596" "5038376" "4910776" "4907275" "4797928" "4634808" "4543646" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:39 |
| L11 | 226 | L10 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:40 |
| L12 | 115 | ((hardware or IC or circuit or chip) with (encrypt\$3 or decrypt\$3 or crypto\$)) and L11 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:41 |
| L13 | 64 | ((combinat\$3 or combin\$3 or multipl\$3 or several or many or group\$3) with (logic or algorithm or method or function\$3)) and L12 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:48 |

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| L14 | 11 | L13 and DES and block and chain\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:48 |
| L15 | 53 | L13 not L14 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:44 |
| L16 | 318 | L8 not L11 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:46 |
| L17 | 143 | ((combinat\$3 or combin\$3 or multipl\$3 or several or many or group\$3) with (logic or algorithm or method or function\$3)) and L16 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:52 |
| L18 | 16 | L17 and DES and block and chain\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:49 |
| L19 | 8850 | "digital signal processor" with (improv\$3 or high\$3 or fast\$3 or enhanc\$3 or quick\$3 or speed\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:51 |
| L20 | 3673 | L19 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 15:04 |
| L21 | 85 | L20 and ("380"/\$.ccls. or "713"/\$.ccls. or "726"/\$.ccls.) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:52 |

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| L22 | 67 | ((combinat\$3 or combin\$3 or multipl\$3 or several or many or group\$3) with (logic or algorithm or method or function\$3)) and L21 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 14:53 |
| L23 | 11 | ("6704871").URPN. | USPAT | OR | ON | 2007/07/22 15:03 |
| L24 | 0 | L23 and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 15:05 |
| L25 | 4 | "6658578" and @ad<"20000126" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/07/22 15:05 |
| L26 | 2 | ("6810475").URPN. | USPAT | OR | ON | 2007/07/22 15:06 |

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 Wolter, S.; Matz, H.; Schubert, A.; Laur, R.;

[Circuits and Systems, 1995. ISCAS '95.. 1995 IEEE International Symposium on Volume 1, 28 April-3 May 1995 Page\(s\):397 - 400 vol.1](#)
 Digital Object Identifier 10.1109/ISCAS.1995.521534

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2. **Security and performance optimization of a new DES data encryption chip**

Verbauwheide, I.; Hoornaert, F.; Vandewalle, J.; De Man, H.J.;
[Solid-State Circuits, IEEE Journal of Volume 23, Issue 3, June 1988 Page\(s\):647 - 656](#)
 Digital Object Identifier 10.1109/4.302

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3. **Speech encryption based on fast Fourier transform permutation**

Borujeni, S.E.;
[Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International Conference on Volume 1, 17-20 Dec. 2000 Page\(s\):290 - 293 vol.1](#)
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4. **Concurrent error detection in block ciphers**

Fernandez-Gomez, S.; Rodriguez-Andina, J.J.; Mandado, E.;
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 Digital Object Identifier 10.1109/TEST.2000.894310

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5. **Reusable cryptographic VLSI core based on the SAFER K-128 algorithm with 251.8 Mbit/s throughput**

Schubert, A.; Meyer, V.; Anheier, W.;
[Signal Processing Systems, 1998. SIPS '98. 1998 IEEE Workshop on 8-10 Oct. 1998 Page\(s\):437 - 446](#)
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6. **'SUPERCRYPT' ASIC technology facilitates a new device family for data encryption**
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1-5 June 1992 Page(s):356 - 359
Digital Object Identifier 10.1109/EUASIC.1992.228002
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7. **A cryptographic chip for ISDN and high speed multi-media applications**
Daemen, J.; Claesen, L.; Genoe, M.; Peeters, G.; Govaerts, R.; Vandewalle, J.;
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8. **An architecture of $F(2^{2N})$ multiplier for elliptic curves cryptosystem**
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Volume 1, 28-31 May 2000 Page(s):279 - 282 vol.1
Digital Object Identifier 10.1109/ISCAS.2000.857084
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9. **Advanced VLSI validated input security device employing data and hardware validation features**
Iliev, V.; Dlay, S.S.; McLauchlan, M.R.; Koelmans, A.M.; Kinniment, D.J.;
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10. **Rapid prototype of a fast data encryption standard with integrity processing for cryptographic applications**
Guendouz, H.; Bouaziz, S.;
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Volume 6, 31 May-3 June 1998 Page(s):434 - 437 vol.6
Digital Object Identifier 10.1109/ISCAS.1998.705304
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11. **Radiation tolerant digital signal processor transformation**
Sampson, S.; Ramaswamy, S.; Alcorn, C.; Haddad, N.; Lawson, D.; Page, T.; Scandalis, G.;
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12. **CADIC: computer-aided design on internet with cryptosystem**
Dong-Eun Lee; Seung-II Kang; Jae-Hong Song; Juho Kim;
Systems, Man, and Cybernetics, 1998. 1998 IEEE International Conference on
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13. **ASIC cryptographical processor based on DES**
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- 14. A Fast VLSI Multiplier for GF(2^m)**
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- 15. Subterranean: A 600 Mbit/sec cryptographic VLSI chip**
Claesen, L.; Daemen, J.; Genoe, M.; Peeters, G.;
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Digital Object Identifier 10.1109/ICCD.1993.393304
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- 16. Design and implementation of RSA cryptosystem using multiple DSP chips**
Er, M.H.; Wong, D.J.; Sethu, A.A.; Ngeow, K.S.;
[Circuits and Systems, 1991., IEEE International Symposium on](#)
11-14 June 1991 Page(s):49 - 52 vol.1
Digital Object Identifier 10.1109/ISCAS.1991.176270
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- 17. A radix-4 modular multiplication hardware algorithm efficient for iterative modular multiplications**
Takagi, N.;
[Computer Arithmetic, 1991. Proceedings., 10th IEEE Symposium on](#)
26-28 June 1991 Page(s):35 - 42
Digital Object Identifier 10.1109/ARITH.1991.145531
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Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000 IEEE International

7-9 Feb. 2000 Page(s):406 - 407

Digital Object Identifier 10.1109/ISSCC.2000.839836

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2. An antifuse EPROM circuitry scheme for field-programmable repair in DRAM

Jae-Kyung Wee; Woodward Yang; Eui-Kyu Ryou; Joe-Sun Choi; Seung-Han Ahn; Jin-Yong Chung; Sea-Chung Kim;

Solid-State Circuits, IEEE Journal of

Volume 35, Issue 10, Oct. 2000 Page(s):1408 - 1414

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 1. Synthesis of robust delay-fault-testable circuits: practice

Devadas, S.; Keutzer, K.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
 Volume 11, Issue 3, March 1992 Page(s):277 - 300
 Digital Object Identifier 10.1109/43.124416

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 2. Design of integrated circuits fully testable for delay-faults and multifaults

Devadas, S.; Keutzer, K.;
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 10-14 Sept. 1990 Page(s):284 - 293
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 3. A single-chip FPGA implementation of the data encryption standard (DES) algorithm

Wong, K.; Wark, M.; Dawson, E.;
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Chiusano, S.; Prietto, P.; Wunderlich, H.-J.;
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 3-5 Oct. 2000 Page(s):644 - 651
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